



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Termerinac et al. **GROUP:** Not yet assigned

SERIAL NO: 10/779,317 **EXAMINER:** Not yet assigned

FILED: February 13, 2004

FOR: COMPENSATION CIRCUIT AND COMPENSATION METHOD TO
COMPENSATE NONLINEAR DISTORTIONS OF AN A/D
CONVERTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

FIRST PRELIMINARY AMENDMENT

Please amend the application as follows:

IN THE CLAIMS:

Cancel claims 21-22 and amend claims 1, 5, 9-11, 13, 14 and 16 as follows:

1. (Currently Amended) Compensation circuit (1-6) to compensate the nonlinear distortions
of an A/D converter, (1) ~~(A/D: analog to digital)~~ comprising

- a signal input and
- a compensation circuit,

characterized by

a digital compensation circuit (2) having the signal input as the digital signal input to
supply nonlinearly distorted digital data (x_n) from the A/D converter (1).